# UDE MCDS Support

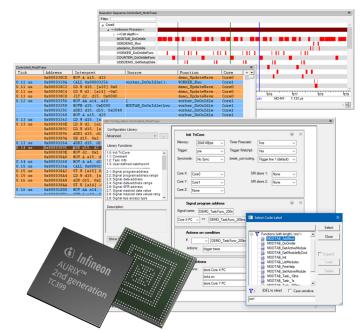
## Multi-Core Trace for Infineon AURIX™ and TriCore™ Devices

Multi-Core-Debug-Solution (MCDS) adds powerful trace and logic analyzer capabilities to AURIX<sup>™</sup> and TriCore<sup>™</sup> microcontrollers from Infineon. The full-featured MCDS is available on Emulation Devices (ED) and adds additional on-chip trace memory, a powerful filter and trigger logic and an optional Aurora Gigabit Trace (AGBT) to the standard production devices without changing the run-time behavior or the pin-out. The on-chip trace memory allows the trace to be recorded and then transferred to the UDE using the JTAG/DAP interface.

### **UDE MCDS Support**

The **UDE MCDS Support** enables the user to use the trace functions offered by MCDS for trace-based debugging, trace-based system analysis and trace-based measurement.

- Universal Emulation Configurator (UEC) a powerful, easy-to-use graphical trace configurator enables access to the complete feature set of MCDS's filter and trigger logic
- Feature-rich trace data analysis functions including profiling, call graph analysis, code coverage
- Visualization of captured traces: execution sequence chart (gantt) chart, trace list with additional information (e.g. data transfers, events, performance counters, and lot more



### miniMCDS – Lightweight Trace for Production Devices

Some AURIX<sup>™</sup> devices are equipped with miniMCDS, which offers limited functionality for trace based debugging even for production devices.

- 8 KB on-chip trace memory
- No performance counters
- Program and data trace for one selectable core
- No option for Aurora Gigabit Trace (AGBT)

## MCDS support for XC2000ED

In addition to the series chip functionality, the XC2000ED emulation device contains also an MCDS-based on-chip emulator providing functions for trace based debugging and system analysis. The internal emulator logic enables a detailed, non-intrusive observation of all relevant system processes on the chip. For recording the program and data flow, a 4 Kbyte on-chip trace memory is available.

For the MCDS of XC2000ED UDE provides the same powerful, easy-to-use graphical trace configurator (UEC) as for the AURIX<sup>™</sup> and TriCore<sup>™</sup> devices.



## UDE MCDS Support

#### Feature Comparison of MCDS, MCDSlight and miniMCDS of AURIX™/TriCore™ and XC2000ED

Features	MCDS					MCDSlight	minMCDS	MCDS for XC2000ED
Supported devices	TC4x	TC37xEXT TC39xED	TC23xED TC24xED TC26xED TC27xED TC29xED	TC1798ED TC1797ED TC1793ED TC1784ED TC1767ED TC172xED	TC1796ED TC1766ED	TC33xED TC35x	TC29x TC37x TC38x	XC2080ED XC2090ED
Program trace								
Compact Function Trace (CFT)							1	
Data trace								
DMA trace								
BUS trace								
Watchpoint (event) trace							1	
Performance counter trace								
Exact timestamping based on ticks								
Multi-core trace (trace multiple cores in parallel)								
GTM trace								
Trace of peripherals								
OCDS L1 trigger condition							1	
Sequential trigger condition							<b>1</b>	
Ref. clock based time stamps							1	
Time based trigger condition							1	
Breaking cores on trigger condition							1	
Emitting signal on external trigger pin							<b>1</b>	
PPU program trace								
Size of on-chip trace memory	Depending on trace mode	Up to 2 MB <sup>2</sup>	Up to 1 MB <sup>2</sup>	Up to 768 KB²	Up to 256 KB <sup>2</sup>	Up to 2 MB <sup>2</sup>	8 KB	4 KB
Aurora Gigabit Trace (AGBT)		■3	3					
Serial Gigabit Trace (SGBT)	3							
Supported target interfaces / connectors	Standard DAP or JTAG, SGBT <sup>3</sup>	Standard DAP or JTAG, AGBT <sup>3</sup>	Standard DAP or JTAG, AGBT <sup>3</sup>	Standard DAP or JTAG	Standard DAP or JTAG	Standard DAP or JTAG	Standard DAP or JTAG	Standard DAP or JTAG
Required UDE licenses	UDE-LIC- TC4xx and UDE-TC UEC	UDE-LIC-TC- MCA and UDE-TC UEC	UDE-LIC-TC- MCA and UDE-TC UEC	UDE-LIC-TC and UDE-TC UEC	UDE-LIC-TC and UDE-TC UEC	UDE-LIC-TC- MCA and UDE-TC UEC	UDE-LIC-TC- MCA, optional UDE-TC UEC <sup>1</sup>	UDE-LIC- XC2000 and UDE-XC2000 UEC

Additional UEC license required.

□ Included in UDE-TC-MCA base license.

<sup>1</sup> Extended features of UEC for miniMCDS not covered by UDE base license. Additional UEC license required.

 $^{\scriptscriptstyle 2}\,$  The actual size of the on-chip trace memory depends on the specific device.

<sup>3</sup> Option for specific devices.

#### If you have any questions about our products, please feel free to contact us:

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